Freeform Search

Database:	US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index	
	IBM Technical Disclosure Bulletins L14 and @py<=1998 20 Documents in Display Format: TI Starting with Number	1
Generate:	C Hit List © Hit Count C Side by Side C Image Search Clear Interrupt	
· ·	Search History	

DATE: Friday, September 30, 2005 Printable Copy Create Case

<u>Set Name</u>	<u>Query</u>	Hit Count	Set Name
ide by side			result set
DB=PC	GPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES	; OP=ADJ	
<u>L15</u>	L14 and @py<=1998	16	<u>L15</u>
<u>L14</u>	analog adj signal\$1 same voltage adj level\$1 same n-bit	42	<u>L14</u>
<u>L13</u>	15 and n-bit	96	<u>L13</u>
<u>L12</u>	16 and bus adj compress\$3	0	<u>L12</u>
<u>L11</u>	L10 and n-bit and data adj values	6	<u>L11</u>
<u>L10</u>	L6 and n-bit	30	<u>L10</u>
<u>L9</u>	L6 and unique adj voltage adj level\$1	1	<u>L9</u>
<u>L8</u>	l6 and n-bit adj data adj value	0	<u>L8</u>
<u>L7</u>	L6 and unique adj voltage adj level\$1	1	<u>L7</u>
<u>L6</u>	analog adj signal\$1 same voltage adj level\$s same range	225	<u>L6</u>
<u>L5</u>	analog adj signal\$1 same voltage adj level\$s	1647	<u>L5</u>
<u>L4</u>	L3 and @py<=1998	7	<u>L4</u>
<u>L3</u>	L2 and analog adj signal\$1 and voltage adj level\$1	12	<u>L3</u>
<u>L2</u>	bus adj compress\$3	232	<u>L2</u>
<u>L1</u>	buss adj decompress\$3	1	<u>L1</u>

Refine Search

Search Results -

Term	Documents
BUS	185713
BUSES	46363
BUSSES	15956
(5 AND BUS).USPT.	38
(L5 AND BUS).USPT.	. 38

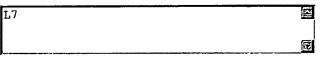
US Pre-Grant Publication Full-Text Database

US Patents Full-Text Database

Database:

US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins

Search:











L1

21561

Search History

DATE: Wednesday, November 17, 2004 Printable Copy Create Case

Set Nam	<u>le Query</u> de	Hit Count	Set Name result set
DB=U	JSPT; PLUR=YES; OP=ADJ		
<u>L7</u>	L5 and bus	38	<u>L7</u>
<u>L6</u>	L5 and bus and compress\$4	0	<u>L6</u>
<u>L5</u>	L3 and bit adj line\$s same voltage adj control\$3	124	<u>L5</u>
<u>L4</u>	L3 and bit adj line\$s same voltge adj control\$3	0	<u>L4</u>
<u>L3</u>	L2 and @py<=1998	900	<u>L3</u>
L2	bit adi line\$s and voltage adi control	2177	'L2

END OF SEARCH HISTORY

bit adj line\$s

<u>L1</u>

Refine Search

Search Results -

Term	Documents
VOLTAGE	747654
VOLTAGES	254843
CONVERTER\$1	0
CONVERTER	272491
CONVERTERA	1
CONVERTERD	20
CONVERTERE	1
CONVERTERS	81384
CONVERTERT	1
CONVERTERY	1
CONVERTER0	5
(L11 AND VOLTAGE ADJ CONVERTER\$1).PGPB,USPT.	1

There are more results than shown above. Click here to view the entire set.

Database:	US Patents Full-Text Database US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins	·	
Search:	L12	Refine	Search
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Search History

DATE: Wednesday, November 17, 2004 Printable Copy Create Case

Set Name side by side

DB=PGPB, USPT; PLUR=YES; OP=ADJ

L12 L11 and voltage adj converter\$1

Hit Count result set

1 L12

<u>L11</u>	L10 and controller same display	43.	<u>L11</u>
L10	bus adj compress\$	162	<u>L10</u>
<u>L9</u>	L5 and bit near2 line\$1	3	<u>L9</u>
<u>L8</u>	L6 and bit near2 line\$1	1	<u>L8</u>
<u>L7</u>	L6 and bit adj line\$1	1	<u>L7</u>
<u>L</u> 6	L5 and @py<=1998	54	<u>L6</u>
<u>L5</u>	two adj voltage adj control	113	<u>L5</u>
	B, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES,	; OP=ADJ	
<u>L4</u>	two adj voltage adj control	232	<u>L4</u>
<u>L3</u>	L1 and two adj voltage adj control\$3	0	<u>L3</u>
<u>L2</u>	L1 and @py<=1998	9	<u>L2</u>
<u> </u>	voltage adj control near2 bit adj line\$1	44	<u>L1</u>

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	Help	Logout	Interrupt

Main Menu Search Form	Show S Numbers	Edit S Numbers	Preferences	Сазев

Search Results -

Term	Documents
BIT.USPT.	238397
BITS.USPT.	164815
DATA.USPT.	727227
DATUM.USPT.	13486
LINE\$1	0
LINE.USPT.	1604802
LINEA.USPT.	207
LINEB.USPT.	5
LINEC.USPT.	7
LINED.USPT.	61686
LINEE.USPT.	31
(L1 AND (BIT OR DATA) ADJ LINE\$1).USPT.	3277

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	US Pre-Grant Publication Full-Text Database JPO Abstracts Database
D ()	EPO Abstracts Database Derwent World Patents Index
Database:	
Search:	Refine Search

Clear

Search History

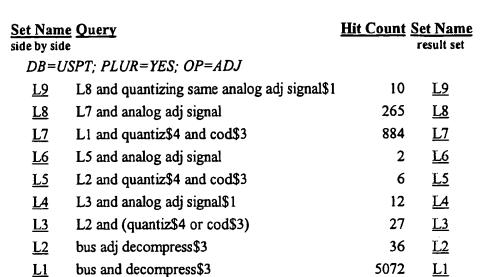
DATE: Wednesday, November 06, 2002 Printable Copy Create Case

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Set Name side by side	Query	Hit Count	Set Name result set
DB=US	PT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ	•	
<u>L16</u>	compressed adj analog adj signal	99	<u>L16</u>
<u>L15</u>	bus adj decompress\$4 and analog adj signal\$1	16	<u>L15</u>
DB=PG	PB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
<u>L14</u>	bus adj compress\$4 same analog adj (out\$4 or signal\$1)	3	<u>L14</u>
DB = US	PT; PLUR=YES; OP=ADJ		
<u>L13</u>	bus adj compress\$4 same analog adj (out\$4 or signal\$1)	1	<u>L13</u>
<u>L12</u>	bus adj compress\$4 and analog adj (out\$4 or signal\$1)	18	<u>L12</u>
<u>L11</u>	bus adj compress\$4 and analog adj (out%4 or signal\$1)	18	<u>L11</u>
<u>L10</u>	bus adj compress\$4 and analog	38	<u>L10</u>
<u>L9</u>	L5 and analog adj signal	18	<u>L9</u>
<u>L8</u>	L7 and analog adj signal	1	<u>L8</u>
<u>L7</u>	L6 and adder	1	<u>L7</u>
<u>L6</u>	L5 and voltage adj control\$3	7	<u>L6</u>
<u>L5</u>	bus adj compress\$4	98	<u>L5</u>
<u>L4</u>	L3 and analog adj signal	1	<u>L4</u>
<u>L3</u>	L1 and adder	1	<u>L3</u>
<u>L2</u>	L1 and voltage adj control\$3	1	<u>L2</u>
<u>L1</u>	bus adj compress\$4 and bus adj decompress\$4	11	<u>L1</u>

Set Name Query side by side DB=USPT; PLUR=YES; OP=ADJ		Hit Count	Set Name result set
	••	,	T 10
<u>L12</u>	L9 and level adj detect\$3	1	<u>L12</u>
<u>L11</u>	L9 and output adj signal\$1	7	<u>L11</u>
<u>L10</u>	L9 and outpput adj signal\$1	0	<u>L10</u>
<u>L9</u>	L8 and voltage adj level\$1 and chang\$3	12	<u>L9</u>
<u>L8</u>	L3 and voltage adj level\$1	12	<u>L8</u>
<u>L7</u>	L6 and bus adj compress\$3	0	<u>L7</u>
<u>L6</u>	voltage adj conver\$4	18360	<u>L6</u>
<u>L5</u>	L3 and voltage adj conver\$4	0	<u>L5</u>
<u>L4</u>	L3 and voltage adj converter\$3	0	<u>L4</u>
<u>L3</u>	bus adj compress\$3	97	<u>L3</u>
<u>L2</u>	L1 and voltage adj convert\$3	0	<u>L2</u>
<u>L1</u>	bus adj compressing	9	<u>L1</u>

Set Name Query side by side		Hit Count	Set Name result set		
DB=USPT; PLUR=YES; OP=ADJ					
<u>L11</u>	L10 and bus same compression	0	<u>L11</u>		
<u>L10</u>	L9 and bus	25	<u>L10</u>		
<u>L9</u>	L6 and controller	52	<u>L9</u>		
<u>L8</u>	L6 and n adj voltage adj converters	0	<u>L8</u>		
<u>L7</u>	L6 and bus adj compression	0	<u>L7</u>		
<u>L6</u>	L5 and compression	126	<u>L6</u>		
<u>L5</u>	L4 and output adj signal	1995	<u>L5</u>		
<u>L4</u>	L3 and voltage adj level\$1	3149	<u>L4</u>		
<u>L3</u>	voltage adj converter\$3	10325	<u>L3</u>		
<u>L2</u>	L1 and voltage adj converter\$3	0	<u>L2</u>		
<u>L1</u>	bus adj compress\$3	97	<u>L1</u>		



Set Name Query side by side		Hit Count Set Name result set				
DB=USPT; PLUR=YES; OP=ADJ						
<u>L28</u>	L26 and second adj voltage adj control\$3	35	<u>L28</u>			
<u>L27</u>	L26 and first adj voltage adj control\$3	33	<u>L27</u>			
<u>L26</u>	L23 and first adj resistor and second adj resistor	1339	<u>L26</u>			
<u>L25</u>	L23 and first adj resistor	1494	<u>L25</u>			
<u>L24</u>	123 and first adj voltage adj control adj means	0	<u>L24</u>			
<u>L23</u>	11 and voltage adj control and resistor	16814	<u>L23</u>			
<u>L22</u>	11 and adder near5 voltage adj level\$1 and analog adj signal\$1	0	<u>L22</u>			
<u>L21</u>	11 and adder near5 voltage adj level\$1	5	<u>L21</u>			
<u>L20</u>	11 and adder same voltage adj level\$1	70	<u>L20</u>			
<u>L19</u>	L1 and voltage adj control\$3 adj output and analog adj signal and adder	5	<u>L19</u>			
<u>L18</u>	L1 and voltage adj control\$3 adj output same analog adj signal	1	<u>L18</u>			
<u>L17</u>	L1 and voltage adj control\$3 adj output and analog adj signal	70	<u>L17</u>			
<u>L16</u>	L1 and voltage adj control\$3 adj output near5 analog adj signal	0	<u>L16</u>			
<u>L15</u>	L1 and voltage adj control\$3 adj output	459	<u>L15</u>			
<u>L14</u>	L13	16	<u>L14</u>			
<u>L13</u>	L12 and output	16	<u>L13</u>			
<u>L12</u>	l2 and Voltage adj control near2 bit adj line\$1	17	<u>L12</u>			
<u>L11</u>	12 and Voltage adj control near3 bit adj line\$1	37	<u>L11</u>			
<u>L10</u>	12 and Voltage adj control near5 bit adj line\$1	61	<u>L10</u>			
<u>L9</u>	12 and Voltage adj control near5 (bit or data) adj line\$1	69	<u>L9</u>			
<u>L8</u>	L4 and bus adj lines	31	<u>L8</u>			
<u>L7</u>	L4 and bus and decompress\$3	1	<u>L7</u>			
<u>L6</u>	L5 and bus and decompress\$3	0	<u>L6</u>			
<u>L5</u>	L4 and voltage adj control adj means	0	<u>L5</u>			
<u>L4</u>	12 and Voltage adj control same bit adj line\$1	555	<u>L4</u>			
<u>L3</u>	12 and Voltage adj control same (bit or data) adj line\$1	721	<u>L3</u>			
<u>L2</u>	L1 and (bit or data) adj line\$1	3277	<u>L2</u>			
<u>L1</u>	voltage adj control	30111	<u>L1</u>			